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PATENT ABSTRACTS OF JAPAN

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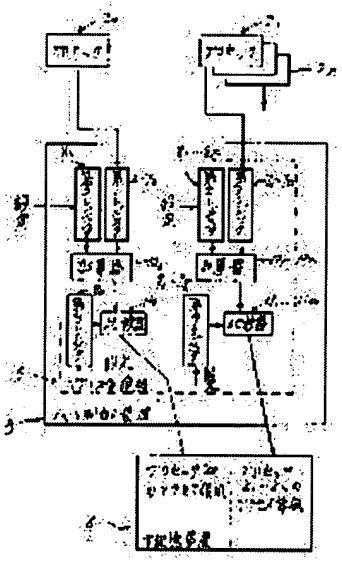
(54) TEST SYSTEM FOR MULTIPROCESSOR SYSTEM

(57)Abstract:
PURPOSE: To test a defective processor without exerting influence upon the normal operation of a processor by restarting a system after distinguishing an access area in a main storage device for a processor from an access area in the main storage device for other processors.
CONSTITUTION: When a fault is generated in a processor 20, the operation of the system is temporarily stopped and respective set values of the 2nd and 3rd registers 80, 90 for the processor 20 are set up differently from respective set values of the 2nd registers 81W8n and the 3rd registers 91W9n for other processors 21W2n. Thereby, the system is restarted after distinguishing the access area in the main storage device 4 for the processor 20 from the access area in the main storage device 4 for the processors 21W2n. Consequently, the defective processor 20 can be tested without disturbing the processors 21W2n in normal operation.



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